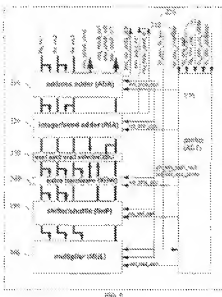


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Abstract of TW 497074 (8)

The present invention is directed to a processor element, such as a microprocessor or a microcontroller, structured to execute an integer instruction set architecture. In a specific embodiment, the present invention provides a method for loading an arbitrary constant number into a memory location, through a series of immediate integer instructions. In another specific embodiment, the present invention provides a method for normalizing a number. The method includes, counting a total number of sign bits in the binary number and then determining the number of sign bits to be shifted from the total number. The result may be used for normalizing a number by left-shifting the binary number by the result.

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